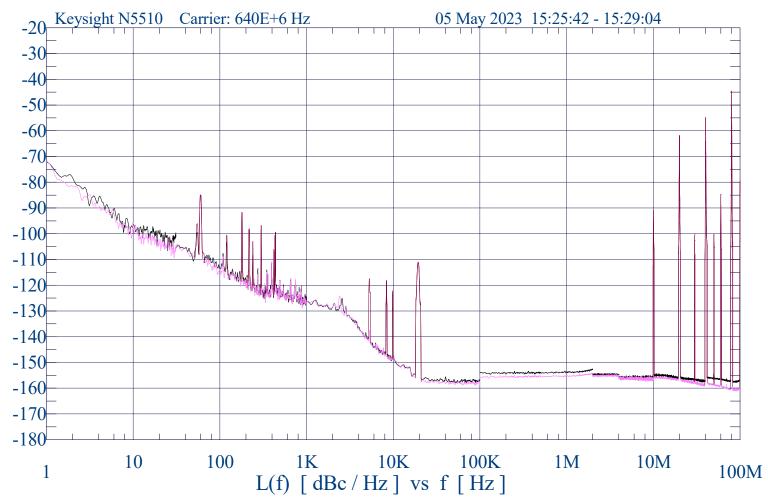
HP8663A Rear 640 OPT 3 v. HP8663A Rear 640 OPT 3



HP8663A Rear 640 OPT 3 v. HP8663A Rear 640 OPT 3 Measurement time: 05 May 2023 15:25:42 - 15:29:04

Measurement type: Absolute phase noise (using a phase locked loop)

Start offset frequency: 1 Hz Stop offset frequency: 100E+6 Hz Minimum number of FFT averages: 10

Minimum number of cross correlations: unknown

RBW percent: unknown FFT overlap percent: unknown FFT Window Type: unknown

Carrier Source frequency: 640E+6 Hz
Carrier Source power: 10 dBm
Detector input frequency: 640E+6 Hz
Reference Source power: 13 dBm
Detector: Automatic detector selection
Nominal VCO tune constant: 5 Hz/Volt

VCO center voltage: 0 Volts VCO tune range: 5 Volts

Detector constant cal method: Derive from measured beatnote.

Detector constant: 311.1E-3 V/Rad

VCO tune constant cal method: Use the current Tune Constant.

Current VCO tune constant: 8.621 Hz/Volt

PLL Integrator attenuation: 0 dB

Phase Locked Loop suppression was NOT verified.

Closed PLL BW: 10.123 Hz Peak Tune Range: 20.294 Hz Assumed Pole: 4E+3 Hz Carrier Source: (manual)

Reference Source: Keysight 8663A; VCO tuned using EFC.

Time Base: (none)